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(C) in bits (MOVQ_R64_XMM %arg4, %xmm1 pslldq $8, %xmm15 # %xmm15 = len(A)||0x0000000000000000

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Copyright (C) 2001 Russell King, * and on early_printk.c by Andi Kleen. * * This is for use before the serial driver has initialized, in * particular, before the UARTs have been discovered and nam

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Copyright (C) 2007 John Williams * * Reasonably optimised generic C-code for memcpy on Microblaze * This is generic C code to do efficient, alignment-aware memcpy.

Copyright (C) 2012 Texas Instruments, Inc. * Paul Walmsley * * A small number of IP blocks need custom reset and preprogramming * functions. The stubs in this file provide a standard way for the

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Copyright 2008 by Martin Bachem (info@bachem-it.com)

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Copyright (c) 2009 Daniel Mack <daniel@caiaq.de> * * Based on code written by * Rodolfo Giometti <giometti@linux.it> * Eurotech S.p.A. <info@eurotech.it>

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Copyright (c) by Jaroslav Kysela <perex@perex.cz> * Routines for the GF1 MIDI interface - like UART 6850 *

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(C) 2000 In-System Design, Inc. (support@in-system.com) * * The ISD200 ASIC does not natively support ATA devices. The chip * does implement an interface, the ATA Command Block (ATACB) which provi

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Copyright (c) 2009 Simtec Electronics * http://armlinux.simtec.co.uk/ * Ben Dooks <ben@simtec.co.uk> * * S3C24XX CPU Frequency scaling - utils for S3C2410/S3C2440/S3C2442

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(C) 2006-2007 MontaVista Software\n"); /* make sure error reporting method is sane */ switch (edac_op_state) { case EDAC_OPSTATE_POLL:

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Copyright (C) Riccardo Facchetti 1995-98\n"); if (io == -1 || dma == -1 || irq == -1) { printk(KERN_INFO "aedsp16: I/O, IRQ and DMA are mandatory\n"); return -EINVAL; }

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Copyright (c) 2005-2008 Joern Engel <joern@logfs.org> */#include "logfs.h"#include <linux/slab.h>#include <linux/writeback.h>#include <linux/backing-dev.h>

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COPYRIGHT 0x01#define DVD_STRUCT_DISCKEY 0x02#define DVD_STRUCT_BCA 0x03#define DVD_STRUCT_MANUFACT 0x04

COPYRIGHT); } return pci_register_driver(}

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Copyright (C) 2003-2004, Paul Clements, SteelEye Technology, Inc. */#ifndef BITMAP_H#define BITMAP_H 1

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(C) 2000 Andrea Arcangeli <andrea@suse.de> SuSE * Queue request tables / lock, selectable elevator, Jens Axboe <axboe@suse.de> * kernel-doc documentation started by NeilBrown <neilb@cse.unsw.edu.au

Copyright (C) 2013 Tom Gundersen <teg@jklm.no> * * This code registers /sys/firmware/efi{,/efivars} when EFI is supported, * allowing the efivarfs to be mounted or the efivars module to be loaded.

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Copyright unasserted) * AN = 0 (Audio data) * P = 0 (Consumer) */ emu->spdif_bits[0] = emu->spdif_bits[1] = emu->spdif_bits[2] = SPCS_CLKACCY_1000PPM

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Copyright (C) 1999 Walt Drummond <drummond@valinux.com> * * 12/26/04 S.Siddha, G.Jin, R.Seth * Add multi-threading and multi-core detection * 11/12/01 D.Mosberger Convert get_cpuinfo() to seq_fi

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Copyright (c) 2006 Herbert Xu <herbert@gondor.apana.org.au> * CTR part based on code (crypto/ctr.c) by:

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copyright info and historical * changelogs. */#include <linux/capability.h>#include <linux/clocksource.h>#include <linux/workqueue.h>#include <linux/hrtimer.h>#include <linux/jiffies.h>#includ

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Copyright message. * 0.0.17 *   Implement Mic and Line in Capture. * 0.0.18 *   Add support for mute control on SB Live 24bit (cards w/ SPI DAC) * * This code was initially based on code fr

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Copyright (C) Hannu Savolainen 1993-1997");   if (!probe_uart401(&cfg_mpu, THIS_MODULE))
        return -ENODEV;   }

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Copyright (c) IBM Corporation 2009# Author: Mel Gorman <mel@csn.ul.ie>use strict;use Getopt::Long;

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Copyright (C) 2001 MIPS Technologies, Inc. */#include <linux/kernel.h>#include <linux/sched.h>#include <linux/signal.h>#include <linux/module.h>#include <asm/branch.h>#include <asm/cpu.h>#incl

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Copyright (C) 2004 Liu Peng Infineon IFAP DC COM CPE

Copyright (c) 1997 Dan Malek (dmalek@jlc.net). */#define BD_SC_EMPTY ((ushort)0x8000) /* Receive is empty */#define BD_SC_READY ((ushort)0x8000) /* Transmit is ready */#defin

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Copyright (C) 1996,1997 Thomas K. Dyas (tdyas@eden.rutgers.edu) * * --- Notes from Thomas's original driver --- * This is the lowlevel driver for the AMD7930 audio chip found on all * sun4c machin

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Copyright (C) 2007 OnStor, Inc. * * This driver is intended to make the second of two hardware watchdogs * on the Sibyte 12XX and 11XX SoCs available to the user. There are two * such devices ava

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Copyright (c) 2009 emlix GmbH * Authors: Oskar Schirmer <oskar@scara.com> * Johannes Weiner <hannes@cmpxchg.org> * Daniel Gloeckner <dg@emlix.com> */#include <linux/bitops.h>#include <linux/ke

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copyright bit for channel status * [4:2] set additional information for channel status * [1:0] set clock accuracy for channel status */ hdmi_write(hdmi, 0x00, HDMI_AUDIO_STATUS_BITS_SETTING_2);

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Copyright (C) 2003, 2004 Richard Curnow * * Provides a debugfs file that lists out the ASIDs currently associated * with the processes.

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copyright(struct cdrom_device_info struct packet_command *cgc){ int ret; u_char buf[8]; struct cdrom_device_ops *cdo = cdi->ops;

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Copyright (C) 2006 * Simon Schulz (ark3116_driver <at> auctionant.de) * * ark3116 * - implements a driver for the arkmicro ark3116 chipset (vendor=0x6547, * productid=0x0232) (used in a datac

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reg_write_range(priv, REG_CH_STAT_B(0), buf, 4);

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EEPROM_MAC_REV   (70)#define EEPROM_SERIAL   (112)#defin

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Copyright : %s\n", (status snd_iprintf(buffer, "Emphasis : %s\n", emphasis[(status & SPCS_EMPHASISMASK) >> 3]); snd_iprintf(buffer, "Mode : %i\n", (status

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COPYRIGHT)) sbits if (val & AC_DIG1_LEVEL) sbits |= (IEC958_AES1_CON_ORIGINAL << 8); sbits |= val & (0x7f << 8); } return sbits;

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Copyright (C) 2006 Andrew Victor * * KS8695 - LAN Registers and bit definitions.

(C) 1999 Rusty Russell (rusty@rustcorp.com.au). */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/in.h>#include <linux/in6.h>#include <linux/ip.h>#include <linux/ipv6.h>#include <li

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(C) pxor GHASH_MUL %xmm8, %xmm13, %xmm9, %xmm10, %xmm11, %xmm5, %xmm6 # final GHASH computation movdqa SHUF_MASK(%rip), %xmm10 PSHUFB_XMM %xmm10, %xmm8 # perform a 16 byte swap

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Copyright(c) 2003-2006 Intel Corporation"#define DRV_VERSION IPW2200_VERSION

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Copyright (c) 2008 Spansion Inc. * Frank Edelhaeuser <frank.edelhaeuser at spansion.com> * (converted to "new style" I2C driver model, removed checkpatch.pl warnings) * * Based on ad7418.c

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Copyright (C) Mike Christie, 2004 - 2005

Copyright (C) 1990, 1999, 2000 Silicon Graphics, Inc. */#ifndef _ASM_BCM63XX_SPACES_H#define _ASM_BCM63XX_SPACES_H

(C) 2005 Pablo Neira Ayuso <pablo@eurodev.net>

Copyright (C) 2006 ST Microelectronics Ltd. (denorm support) * * FIXME! These routines have not been tested for big endian case.

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Copyright 2002 Andi Kleen, SuSE Labs. */#define pr_fmt(fmt) "AGP: " fmt

Copyright (C) 2001 Georges Menie, Ken Desmet

Copyright (c) 2006, Michael Wu <flamingice@sourmilk.net> * * Based on the islsm (softmac prism54) driver, which is:

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Copyright IBM Corp. 1999, 2006 * Author(s): Denis Joseph Barrow (djbarrow@de.ibm.com,barrow_dj@yahoo.com) * * Based on Intel version

(C) 1998 Stephen Tweedie */#include <linux/mm.h>#include <linux/gfp.h>#include <linux/kernel_stat.h>#include <linux/swap.h>#include <linux/swapops.h>#include <linux/init.h>#include <linux/page

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Copyright (c) 2012 Bj * * The probing code is heavily inspired by cdc_ether, which is:

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COPYRIGHT)) val if (sbits & (IEC958_AES1_CON_ORIGINAL << 8)) val |= AC_DIG1_LEVEL; val |= sbits & (IEC958_AES1_CON_CATEGORY << 8); } return val;

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(C) have a thousand of cores and user configured it to 32 partitions * MDS_NTHRS_BASE(64) * 32 = 2048 * * which is already above soft limit MDS_NTHRS_MAX(1024), but we still need * to guarante

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(C) in bits (MOVQ_R64_XMM %arg4, %xmm1 pslldq $8, %xmm15 # %xmm15 = len(A)||0x0000000000000000

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Copyright IBM Corp. 2011 * Author(s): Jan Glauber <jang@linux.vnet.ibm.com> */#include <linux/hugetlb.h>#include <linux/module.h>#include <linux/mm.h>#include <asm/cacheflush.h>#include <asm/fa

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Copyright 2009 Integrated Device Technology, Inc. * Alex Bounine <alexandre.bounine@idt.com> * - Added Port-Write message handling * - Added Machine Check exception handling *

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Copyright (c) International Business Machines Corp., 2002,2008 *   Author(s): Steve French (sfrench@us.ibm.com) * *   Error mapping routines from Samba libsmb/errormap.c

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Copyright (c) 2010 Samsung Electronics Co., Ltd. *   http://www.samsung.com/ * * GPIO configuration for S3C64XX KeyPad device

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Copyright 2009 Sysgo AG * Thomas Moll <thomas.moll@sysgo.com> * - fixed maintenance access routines, check for aligned access *

copyright(int port){ int len = 0; int ret;

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Copyright (C) Joerg Reuter DL1BKE (jreuter@yaina.de) */#include <linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include <linux/spinlock.h>#include <linux/in.h>#include <linux/k

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Copyright (c) by Jaroslav Kysela <perex@perex.cz> * Routines for control of EMU10K1 MPU-401 in UART mode *

Copyright (C) 2004 Dirk Behme <dirk.behme@de.bosch.com>

Copyright (C) 2008, Guennadi Liakhovetski <kernel@pengutronix.de> * * This driver provides an interface between platform-specific camera * busses and camera devices. It should be used if the camera

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copyright copyright;struct dvd_disckey disckey; struct dvd_bca bca; struct dvd_manufact manufact;} dvd_struct;

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Copyright (C) 2010 Red Hat, Inc., Peter Zijlstra <pzijlstr@redhat.com> * * Provides a framework for enqueueing and running callbacks from hardirq * context. The enqueueing is NMI-safe.

Copyright (C) 1993 Linus Torvalds * * Delay routines calling functions in arch/metag/lib/delay.c

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Copyright (C) 1992 Eric Youngdale * Simulate a host adapter with 2 disks attached. Do a lot of checking * to make sure that we are not getting blocks mixed up, and PANIC if * anything out of t

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Copyright (c) 2007-2008, Realsil Wlan\n"); RT_TRACE(COMP_INIT, "Initializing module"); RT_TRACE(COMP_INIT, "Wireless extensions version %d", WIRELESS_EXT); rtl8192_proc_module_init(); return usb_r

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Copyright string). This works with any SB and compatible. - Added the code to request_region at device init (should go in the main body of voxware). v0.4 (BETA) - Better configure.c pat

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Copyright (C) 1996 Paul Mackerras. */#include <stdarg.h>#include <linux/types.h>#include <linux/errno.h>#include <linux/kernel.h>#include <linux/delay.h>#include <linux/adb.h>#include <linux/c

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Copyright (C) 2013, Seth Jennings, IBM * * Concepts based on zcache internal zbud allocator by Dan Magenheimer.

Copyright (C) 2005-2006 Micronas USA Inc.static int go7007_usb_vendor_request(struct go7007 *go, u16 request, u16 value, u16 index, void *transfer_buffer, int length, int in){ struct go7007_usb *u

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(C) * Notes: To enter USB suspend mode * Command Mode */struct usb_suspend_parm { u32 action; /* 1: sleep, 0:resume */

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(C) Copyright 2005 Greg Kroah-Hartman <gregkh@suse.de> * * notifier functions originally based on those in kernel/sys.c * but fixed up to not be so broken.

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Copyright IBM Corp. 1999,2001 * * Device mapping and dasd= parameter parsing functions. All devmap * functions may not be called from interrupt context. In particular * dasd_get_device is a no-no

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Copyright (c) 1998-2001 Axis Communications AB * * csum_partial(const unsigned char * buff, int len, unsigned int sum)

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Copyright (C) 1994-2003 Dario Ballabio.\n"); printk ("%s config options -> tm:%d, lc:%c, mq:%d, rs:%c, et:%c, " "ip:%c, ep:%c, pp:%c.\n", driver_name, tag_mode, YESNO(linked_comm

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(C) 1999 Andrea Arcangeli <andrea@suse.de> (dynamic inode allocation) */#include <linux/export.h>#include <linux/fs.h>#include <linux/mm.h>#include <linux/backing-dev.h>#include <linux/hash.h>#

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(C) 1997 M. Kirkwood * Converted to new API by Alan Cox <alan@lxorguk.ukuu.org.uk> * Various bugfixes and enhancements by Russell Kroll <rkroll@exploits.org> * * Converted to the radio-isa framewo

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Copyright 2000 Silicon Graphics, Inc. */#ifndef __ASM_SN_MAPPED_KERNEL_H#define __ASM_SN_MAPPED_KERNEL_H

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Copyright (C) 2007 Intel Corp * Fenghua Yu <fenghua.yu@intel.com> * Add multiple ptc.g/ptc.ga instruction support in global tlb purge. */#include <linux/module.h>#include <linux/init.h>#include

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copyright not found\n");#endif /* Validate checksum */ for (i = s = 0; i < EEPROM_CHECKSUM; i++) s += e[i]; s &= 0xFF; if (s != e[EEPROM_CHECKSUM]) { printk(KERN_ERR DEV_LABEL "(itf %d): EEPR

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Copyright (c) by Scott McNab <sdm@fractalgraphics.com.au> * * Trident 4DWave-NX memory page allocation (TLB area) * Trident chip can handle only 16MByte of the memory at the same time.

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Copyright (c) 2004 James Courtier-Dutton <James@superbug.demon.co.uk> * Driver CA0106 chips. e.g. Sound Blaster Audigy LS and Live 24bit * Version: 0.0.25 * * FEATURES currently supported:

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Copyright (C) 2013 Davidlohr Bueso <davidlohr@hp.com> * * futex-requeue: Block a bunch of threads on futex1 and requeue them * on futex2, N at a time.

Copyright (C) 1996 Paul Mackerras. * * This file contains low-level assembler routines for managing * the PowerPC MMU hash table. (PPC 8xx processors don't use a * hash table, so this file is

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Copyright (C) 2011 Anssi Hannula <anssi.hannula@ * * This file is based on a keytable provided by * Jan Losinski <losinski@wh2.tu-dresden.de>

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Copyright (C) 2014 Dan Streetman * * This is a common frontend for memory storage pool implementations. * Typically, this is used to store compressed memory.

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Copyright (C) 2008-2011 Nokia Corporation * * Written by Paul Walmsley and Jouni Högander * Added OMAP4 specific support by Abhijit Pagare <abhijitpagare@ti.com>

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Copyright (C) 2010, Arnaldo Carvalho de Melo <acme@redhat.com> */#include <sys/types.h>#include <sys/time.h>#include <time.h>#include <dirent.h>#include <unistd.h>#include "builtin.h"#include

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Copyright IBM Corp. 2006, 2007 * Author(s): Jan Glauber <jan.glauber@de.ibm.com> * Driver for the s390 pseudo random number generator */#include <linux/fs.h>#include <linux/init.h>#include <linu

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Copyright (C) Hitachi Ltd., 2012 */#include <linux/kprobes.h>#include <linux/ptrace.h>#include <linux/hardirq.h>#include <linux/preempt.h>#include <linux/ftrace.h>

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Copyright (C) 2008 Compulab, Ltd. * Mike Rapoport <mike@compulab.co.il>

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Copyright (C) 2003 Anton Blanchard <anton@au.ibm.com>, IBM * * pSeries specific routines for PCI.

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(C) 2008, 2009 by Marc Kleine-Budde <kernel@pengutronix.de> * * Bosch C_CAN controller is compliant to CAN protocol version 2.0 part A and B. * Bosch C_CAN user manual can be obtained from:

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Copyright (C) 2010 Nokia Corporation * Contact: Yauheni Kaliuta <yauheni.kaliuta@nokia.com> * * The driver borrows from ether.c which is: *

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copyright: failed return -ENODEV; }

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Copyright (C) 2002 Anton Blanchard <anton@au.ibm.com>, IBM

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COPYRIGHT);   snd_emu10k1x_ptr_write(chip, SPCS1, 0,   chip->spdif_bits[1] =   SPCS_CLKACCY_1000PPM | SPCS_SAMPLERATE_48 | SPCS_CHANNELNUM_LEFT | SPCS_SOURCENUM_UNSPEC |

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Copyright (C) 2003 Jens Axboe <axboe@kernel.dk> */#include <linux/module.h>#include <linux/slab.h>#include <linux/blkdev.h>#include <linux/elevator.h>#include <linux/jiffies.h>#include <linux/r

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Copyright (C) 2008 Jiri Hladky <hladky _dot_ jiri _at_ gmail _dot_ com> * * parisc implementation:

Copyright (C) 2011 Renesas Solutions Corp. * * Based on pxa2xx_spi.c:

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(C) 2012 Google, Inc. * Author: Kent Overstreet <koverstreet@google.com> * * This implements a refcount with similar semantics to atomic_t - atomic_inc(), * atomic_dec_and_test() - but percpu.

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Copyright(c) 2002-2010 Exar Corp.\n"); DBG_PRINT(ERR_DBG, "%s: Neterion %s (rev %d)\n", dev->name, sp->product_name, pdev->revision); DBG_PRINT(ERR_DBG, "%s: Driver version %s\n", dev->name,

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Copyright 2002-2003, Stephen Frost, 2.5.x port by laforge@netfilter.org */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/init.h>#include <linux/ip.h>#include <linux/ipv6.h>#include <

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Copyright (C) 2001 Stefan Gmeiner <riddlebox@freesurf.ch> * start merging tpconfig and gpm code to a xfree-input module * adding some changes and extensions (ex. 3rd and 4th button) *

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Copyright 2010 Eric B * * based on sound/soc/s3c24xx/s3c24xx_simtec_tlv320aic23.c

Copyright (C) 1995, 1996, 1997 Olaf Kirch <okir@monad.swb.de> * * This file describes the layout of the file handles as passed * over the wire.

Copyright (C) 2000 David A. Schleef <ds@schleef.org> * * Based on Sensoray Model 626 Linux driver Version 0.2

Copyright (C) IBM Corporation, 2010-2012 * Author: Srikar Dronamraju <srikar@linux.vnet.ibm.com> */

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Copyright 1998 C. van Schaik <carl@leg.uct.ac.za> * * BUGS * Due to the inconsistency in reading from the signal flags * it is difficult to get an accurate tuned signal.

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Copyright (C) 2007 Ralf Baechle <ralf@linux-mips.org> */#include <linux/clockchips.h>#include <linux/interrupt.h>#include <linux/percpu.h>#include <linux/smp.h>#include <linux/irq.h>

Copyright 2001 by Micro Solutions, Inc., DeKalb IL.\n",pi->device); printk("%s: BACKPACK %s, Micro Solutions BACKPACK Drive at 0x%x\n", pi->device,BACKPACK_VERSION,pi->port); printk("%s: Unit: %d

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Copyright 2008 Thomas Bogendoerfer <tsbogend@alpha.franken.de> * Mxier part taken from mace_audio.c:

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(C) (diagram), 9.2 * Upon reception of the SHUTDOWN COMPLETE chunk the endpoint will verify * that it is in SHUTDOWN-ACK-SENT state, if it is not the chunk should be * discarded. If the endpoint is

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Copyright message. * 0.0.19 * Finally fix support for SB Live 24 bit. SB0410 and SB0413. * The output codec needs resetting, otherwise all output is muted. * 0.0.20 * Merge "pci_disabl

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(C) 2004 Kalev Lember <kalev@smartlink.ee> * * Author: David Woodhouse <dwmw2@infradead.org> * Additional Diskonchip 2000 and Millennium support by Dan Brown <dan_brown@ieee.org> * Diskonchip Mill

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hdmi_reg_writeb(hdata, HDMI_I2S_CH_ST_2, HDMI_I2S_SET_SOURCE_NUM(0));

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Copyright (C) 2007 Red Hat, Inc., Peter Zijlstra <pzijlstr@redhat.com> * * this code maps all the lock dependencies as they occur in a live kernel * and will warn about the following classes of loc

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(C) 2005 by Christian Hentschel <chentschel@arnet.com.ar> * based on RR's ip_conntrack_ftp.c and other modules.

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(C) 2000-2006 Netfilter Core Team\n"); return 0;

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Copyright (C) 2004, 2010 Nokia Corporation * Written by Timo Teras <ext-timo.teras@nokia.com> * * Converted to x_tables and forward-ported to 2.6.34 * by Luciano Coelho <luciano.coelho@nokia.com>

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(C) 2000 Milan Pikula <www@fornax.sk> * * lirc_sir - Device driver for use with SIR (serial infra red) * mode of IrDA on many notebooks.

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Copyright IBM Corp. 2001, 2006 * Author(s): Robert Burroughs * Eric Rossman (edrossma@us.ibm.com) * * Hotplug & misc device support: Jochen Roehrig (roehrig@de.ibm.com) * Major cleanup

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Copyright (C) 1998-2003 Hewlett-Packard Co * David Mosberger-Tang <davidm@hpl.hp.com> */#include <linux/kernel.h>#include <linux/init.h>

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Copyright (C) 2013 Huawei Ltd. * Author: Jiang Liu <liuj97@gmail.com> * * Based on arch/arm/include/asm/jump_label.h

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Copyright (c) 2013 Trond Myklebust <Trond.Myklebust@netapp.com> */#include <linux/nfs_fs.h>#include "nfs4_fs.h"#include "internal.h"#include "nfs4session.h"#include "callback.h"

(C) Copyright IBM Corp. 2005 * * SPU-side context restore sequence outlined in * Synergistic Processor Element Book IV

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Copyright (c) 2006 Arnaldo Carvalho de Melo <acme@conectiva.com.br>

Copyright (C) 2009 William M. Brack * * Refactored and updated to the latest v4l core frameworks: *

Copyright (C) 2011 Texas Instruments, Inc. * Santosh Shilimkar <santosh.shilimkar@ti.com> * * OMAP4430 MPUSS mainly consists of dual Cortex-A9 with per-CPU * Local timer and Watchdog, GIC, SCU, PL

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Copyright (C) 2009 Uwe Kleine-Koenig, Pengutronix * * Common code for i.MX1, i.MX21 and i.MX27

Copyright (C) 2002 Laurent Pinchart <laurent.pinchart@skynet.be> * * $Id: zr36060.h,v 1.1.1.1.2.3 2003/01/14 21:18:47 rbultje Exp $

Copyright (C) 2005-2007 Red Hat GmbH * * A target that delays reads and/or writes and can send * them to different devices.

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(C) 2001 Dimitromanolakis Apostolos <apdim@grecian.net> * * Based in the radio Maestro PCI driver. Actually it uses the same chip * for radio but different pci controller.

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Copyright (C) 2011 ST-Ericsson SA * Written on behalf of Linaro for ST-Ericsson * This interface is used in the core to keep track of pins. * * Author: Linus Walleij <linus.walleij@linaro.org>

(C) 1999 Thomas Bogendoerfer (tsbogend@alpha.franken.de) * * based on code from Oliver Jowett <oliver@jowett.manawatu.gen.nz>

Copyright (C) 1998 Franz Sirl * * Frame buffer structure from:

Copyright (C) 2008 Novell Inc. */#undef DEBUG

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Copyright (C) 2013 Davidlohr Bueso <davidlohr@hp.com> * * futex-hash: Stress the hell out of the Linux kernel futex uaddr hashing.

Copyright (C) 2003 Maciej W. Rozycki

Copyright (C) 1996-2003 Russell King

Copyright (C) 2004 by Ralf Baechle * * The cpustart method is a PMC-Sierra's function to start the secondary CPU. * Stock PMON 2000 has the smpfork, semlock and semunlock methods instead.

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COPYRIGHT; ucontrol->value.iec958.status IEC958_AES1_CON_ORIGINAL; ucontrol->value.iec958.status[3] = IEC958_AES3_CON_FS; return 0;}

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Copyright IBM Corp. 1999 * Author(s): Hartmut Penner (hp@de.ibm.com), * Martin Schwidefsky (schwidefsky@de.ibm.com) * * Derived from "include/asm-i386/processor.h"

Copyright (c) 2009 Tim Blechmann <tim@klingt.org>

Copyright (C) 2006 Compulab, Ltd. * Mike Rapoport <mike@compulab.co.il> * - Creation of driver * * Based on pxafb.c

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Copyright (C) 2006 Benjamin Herrenschmidt, IBM Corporation * * Provide default implementations of the DMA mapping callbacks for * directly mapped busses.

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Copyright (C) 2008-2010 Nokia Corporation * Contact: Felipe Balbi <felipe.balbi@nokia.com> * * This gadget driver borrows from serial.c which is: *

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Copyright 2007 Andi Kleen, SUSE Labs. * Subject to the GPL, v.2 */#include <linux/mm.h>#include <linux/err.h>#include <linux/sched.h>#include <linux/slab.h>#include <linux/init.h>#include <lin

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Copyright (C) 2007 Ralf Baechle (ralf@linux-mips.org) */#include <linux/module.h>#include <linux/init.h>#include <linux/platform_device.h>#include <linux/serial_8250.h>

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Copyright (C) 2006 Intel Co * Xuefei Xu (Anthony Xu) <anthony.xu@intel.com>

Copyright (c) 2002, 2006, 2008 David S. Miller (davem@davemloft.net) */#include <linux/kernel.h>#include <linux/module.h>#include <linux/init.h>#include <linux/input.h>#include <linux/of_device.

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Copyright (c) 2006 Oliver Neukum <oliver@neukum.name> * * USB Printer Device Class driver for USB printers and printer cables

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Copyright (C) 2009 Wind River Systems, * written by Ralf Baechle <ralf@linux-mips.org> */#include <linux/init.h>#include <linux/irqflags.h>#include <linux/notifier.h>#include <linux/prefetch.h

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COPYRIGHT) { snd_iprintf(buffer, "permitted\n"); } else { snd_iprintf(buffer, "protected\n"); } snd_iprintf(buffer, "Emphasis: "); if ((status[0] & IEC958_AES0_CON_EMPHASIS) != IEC958_AE

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copyright;for (;;) while (*++c != '$') if (*c == '\0') exit(0); while (*++c != '$') putc(*c, stderr); putc('\n', stderr); }

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Copyright (C) 2000 David Mosberger-Tang <davidm@hpl.hp.com> * * Generic IA-64 unwind info decoder.

Copyright (C) 2009 Zhang Le <r0bertz@gentoo.org> * * reference: /proc/cpuinfo, * arch/mips/kernel/cpu-probe.c(cpu_probe_legacy), * arch/mips/kernel/proc.c(show_cpuinfo), * loongson2f user manual.

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copyright + flash_ver images, 0 for failure.static u32 chk_cfg(u8 *cfg, u32 length, u32 *flash_ver){ u16 *pw = (u16 *)cfg - 1; u32 sz = 0; u32 len = length;

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(C) 2005 Finn Thain * * Converted to DMA API, and (from the mac68k project) introduced * dhd's support for 16-bit cards.

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(C) State timer broadcast controlstatic void lapic_timer_state_broadcast(struct acpi_processor *pr, struct acpi_processor_cx *cx, int broadcast){ int state = cx - pr->power.st

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(curl 7.57.0)

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